Intel® Graphics

GOP Driver Release Notes

5.5.1033

April 13, 2015

This GOP Driver release contains support for 4th Generation Intel® Core™ Processor Production Versions and 5th Generation Intel® Core Processor Production Versions

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Revision History

Date	Version number	Description
April 13,2015	5.5.1033	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
February 18,2015	5.5.1032	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
October 17,2014	5.5.1031	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
October 10,2014	5.5.1030	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
October 6,2014	5.5.1029	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
September 22, 2014	5.5.1028	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
September 12, 2014	5.5.1027	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
September 4,2014	5.5.1026	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
August 8,2014	5.5.1025	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
July 25,2014	5.5.1024	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
July 14,2014	5.5.1023	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
June 13,2014	5.5.1022	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
May 28, 2014	5.5.1021	PV for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
May 15, 2014	5.5.1020	Beta for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
May 12, 2014	5.5.1019	Beta for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
April 11, 2014	5.5.1018	Beta for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms

Date	Version number	Description
March 21, 2014	5.5.1017	Beta for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
February 27, 2014	5.5.1016	Beta for 5th Generation Intel® Core™ Processor Platforms PV for 4th Generation Intel® Core™ Processor Platforms
February 7, 2014	5.5.1015	Alpha version
January 24, 2014	5.5.1014	Alpha version
December 27, 2013	5.5.1013	Alpha version
November 15, 2013	5.5.1011	Alpha version
November 7, 2013	5.5.1009	Pre-Alpha version
October 18, 2013	5.5.1007	Pre-Alpha version
September 30, 2013	5.5.1006	Pre-Alpha version
August 28, 2013	5.5.1005	Pre-Alpha version
August 22, 2013	5.5.1004	Pre-Alpha version

GOP Driver release contains support for the following Intel Chipsets/Processors:

- 5th generation Intel(R) Core(TM) processors (codename Broadwell)

- 4th Generation Intel® Core™ Processor Platform

- 4th Generation Intel® Core™ Processor based on Mobile U-Processor

- 4th Generation Intel® Core™ Processor based on Mobile Y-Processor

Feature Changes

Major changes		
Reference No.	**Description**	**Version**
[DCN][4136275] **BUG [5557086]**	GOP Driver detects a none existing DVI-Monitor	5.5.1033
[DCN][4136216] **BUG [5641377]**	System sometimes doesn't display during POST under Uefi mode	5.5.1033
[DCN][4133613] **BUG [5618980]**	When HDMI is connected with DP-RGB at the same time, GOP can't find HDMI.	5.5.1032

Major changes

Reference No.	Description	Version
[DCN][3930400]	Correct the Bit mappings for storing the Max cd clock in the Software flag register	5.5.1031
[DCN][3930349]	Change the Vswing and Pre-emph values for the EDP and DP as per new spec	5.5.1030
[DCN][3929849] BUG [5555066]	Display is not coming in GOP on 4K*2K eDP panel in BDW ULT	5.5.1029
[DCN][3929431]	DP standard BufferTranslationTable update from H/W	5.5.1028
[DCN][3928944] /BUG [5556014]	[LCB]External HDMI monitor will black screen when system restart	5.5.1027
[DCN][3928487]	Help text updated for correct Vswing and Preemphasis Values Table of VBT for BDW GOP.	5.5.1026
[DCN][3924561]	Correction in Lane Reversal while reading the lane reversal bit during disable sequence	5.5.1026
[DCN][3924444]	Changing the Gop driver Controller string name by removing the platform specific word	5.5.1025
[DCN][3924247]	Fix for discrepancy in T14 delay(PWM on to Backlight on) between user selection and what got observed in scope	5.5.1025
[DCN][3923641] /BUG [5526151]	New Customer Visible options in VBT are available for edp panel display sequence T14(PWM on -> backlight on) and T15 (backlight off -> PWM off).	5.5.1025
[DCN][3923461]	Have a common VBT/GOP for both HSW and BDW without needing to select platform in BMP tool	5.5.1024
[DCN] [3922808]/ BUG [5486913]	Added WA for EDID 1.4 eDP panel when color depth is undefined in EDID data	5.5.1023
[DCN] [3922755]/ BUG [5526272]	Display blanks-out on eDP display after POST screen on HSW platform Only.	5.5.1023
[DCN][3922111] /BUG [5486514]	Updated Display Port Voltage Swing Pre-Emphasis for edp display	5.5.1023
[DCN][3922147]	Updated Help Text for PPS and few software changes to program T9 delay	5.5.1023
[DCN][3921238]	Correction of color depth programming when EDID of panel returns invalid color depth information.	5.5.1022
[DCN][3920696]	Help text Updated for some VBT options	5.5.1021
[DCN][3920439]	Added voltage_level_0/pre-emphasis_level_3 (v0/p3). Both levels are optional in the Display Port standard, but it turns out that some panels require v0/p3.	5.5.1021

Major changes

Reference No.	Description	Version
[DCN][3919842]	Help text change updated for FLT Vswing and Pre-Emphasis options in VBT	5.5.1020
[DCN] [3919194]	Support added for DDI Lane Reversal	5.5.1019
[DCN] [3918488]	Implementation of High or Low Vswing Pre Emphasis Table for eDP	5.5.1019
[DCN] [3917796]	DP/eDP Link Training changes	5.5.1018
[DCN] [3917570]	While fast link training the pre-emphasis and Vswing values are not set as per the VBT	5.5.1018
[DCN] [3917030]	Updated help text for BDW for display configuration removal table in VBT	5.5.1017
[DCN] [3915656]	Fix for EDID Less CRT display blank out with DP->VGA dongle	5.5.1016
[DCN] [5223772]	Fix for BIST Failure	5.5.1015
[DCN] [3914993]	Fix for QHD+ eDP Panel blank out due to incorrect CD Clock programming	5.5.1014
[DCN] [3881222]	Implement the Fast mode Set for eDP in BDW except the first Native mode set	5.5.1013
[DCN] [3881166]	DP Link Training Porting to BDW	5.5.1013
[DCN] [3880572]	Getting the color depth information from EDID for all the displays	5.5.1012
[DCN] [3880571]	Thunderbolt Monitor is blanking out with GOP driver	5.5.1012
[DCN] [3879972]	Implementation of EFP Panel Color depth based on EDID	5.5.1012
[DCN] [3879985]	During unloading of GOP driver or during Hot plug, Uninstalling of protocols installed on the child handles happens but BIST protocol is not getting uninstalled.	5.5.1012
[DCN] [3879986]	eDP devices showing in EFI shell with single display DP\HDMI connected	5.5.1012
[DCN] [3879492]	Correction in the Detection Sequence for DP to VGA dongle	5.5.1011
[DCN] [3879482]	Hide Windows* XP Related VBT Options in the BSF Script	5.5.1011
[DCN] [3879169]	Correct the mapping of GMS bits to retrieve the frame buffer size allocated by BIOS	5.5.1009
[DCN] [3879332]	Addition of the Device Id's for Broadwell for PCI 0 2 0 (Intel Gfx Device) device	5.5.1009
[DCN] [3878870]	Increase LQA wait time 150ms to 200ms	5.5.1009
[DCN][3878415]	eDP enable and disable sequence were not following the spec.	5.5.1007
[DCN][3878341]	Fix for calculating Max Index Before Identifying Broadwell PCH or 4th Generation Intel® Core™ Processor Platform PCH	5.5.1007
[DCN][3878342]	Add $VBT check	5.5.1007
[DCN][3878339]	Porting Assertive Display Technology feature from 4th Generation Intel® Core™ Processor Platform	5.5.1007

Major changes

Reference No.	Description	Version
[DCN] [3874881]	Determine PCH Type based on Device ID and implement PCH clock Changes Based on PCH Type	5.5.1006
[DCN] [3874423]	Implementation of Fast Link Training Feature for eDP	5.5.1006
[DCN][3873834]	Port enable DMRRS feature from 4th Generation Intel® Core™ Processor Platform to BDW	5.5.1005
[DCN][3873414]	HDMI Level shifter list selection support (4th Generation Intel® Core™ Processor Platform or BDW) in VBT based on chipset	5.5.1004
[DCN][3872905]	Integration of Power on code to the BDW GOP Mainline branch	5.5.1004

Known Issues

Known issues in Build 1033	
None	