

Intel® Graphics

Build# 5.5.1039

Video BIOS Release Notes

May 20, 2015

This Video BIOS release contains support for 4th Generation Intel® Core™ Processor Production Versions and 5th Generation Intel® Core Processor Production Versions

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Revision History

| Date | Build Number | Description |
|--------------------|--------------|--|
| May 20,2015 | 5.5.1039 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| February 18, 2015 | 5.5.1038 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| January 30, 2015 | 5.5.1037 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| November 26, 2014 | 5.5.1036 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| October 17, 2014 | 5.5.1035 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| October 10, 2014 | 5.5.1034 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| October 6, 2014 | 5.5.1033 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| September 22, 2014 | 5.5.1032 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| September 16, 2014 | 5.5.1031 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| August 8,2014 | 5.5.1030 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| July 25,2014 | 5.5.1029 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| July 14,2014 | 5.5.1028 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| June 20,2014 | 5.5.1027 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |

| Date | Build Number | Description |
|--------------------|--------------|--|
| May 28, 2014 | 5.5.1025 | PV for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| May 23, 2014 | 5.5.1024 | Beta for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| May 15, 2014 | 5.5.1023 | Beta for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| May 12, 2014 | 5.5.1022 | Beta for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| April 24, 2014 | 5.5.1021 | Beta for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| April 4, 2014 | 5.5.1020 | Beta for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| March 25, 2014 | 5.5.1019 | Beta for 5 th Generation Intel® Core™ Processor Platforms PV for 4 th Generation Intel® Core™ Processor Platforms |
| February 27, 2014 | 5.5.1017 | Beta |
| February 7, 2014 | 5.5.1016 | Alpha |
| January 24, 2014 | 5.5.1015 | Alpha |
| December 27, 2013 | 5.5.1014 | Alpha |
| December 14, 2013 | 5.5.1013 | Alpha |
| November 15, 2013 | 5.5.1011 | Alpha |
| November 8, 2013 | 5.5.1010 | Pre-Alpha |
| October 18, 2013 | 5.5.1008 | Pre-Alpha |
| September 27, 2013 | 5.5.1007 | Pre-Alpha |
| August 22, 2013 | 5.5.1006 | Pre-Alpha |

Video BIOS release contains support for the following Intel Chipsets/Processors:

- 5th Generation Intel® Core™ Processors (formerly Broadwell)
- 4th Generation Intel® Core™ Processors
- 4th Generation Intel® Core™ Processors based on Mobile U-Processor
- 4th Generation Intel® Core™ Processors based on Mobile Y-Processor

Feature Changes

| Feature addition, removal or enhancements | | |
|---|--|----------|
| Reference No. | Description | Build |
| [DCN] 4139149 [BUG] 5642338 | KVM: kvm session was closed after ~20 seconds. | 5.5.1039 |
| [DCN] 4138872 [BUG] 5619303 | Port the "Display corruption in Clone mode with eDP+HDMI and eDP+DP during Windows Driver Disable" from SKL. | 5.5.1039 |
| [DCN] 4133320 [BUG] 5641661 | VBIOS interrupt code corrupts EAX register | 5.5.1038 |
| [DCN] 4132624 [BUG] 5619303 | No DEV screen display on external 4K DP monitor and can't boot to ChromeOS | 5.5.1037 |
| [DCN] 4131249 [BUG] 5641290 [BUG] 5618009 | BSOD 9F when boot to Win8.1-32bit OS. | 5.5.1037 |
| [DCN] 4011798 [BUG] 5557511 | Backlight off during post logo into windows. | 5.5.1037 |
| [DCN] 3934821 | Correct the HDMI level shifter string (800mV 2.0dB) for HSW CPU | 5.5.1036 |
| [DCN] 3930681 [BUG] 5528443 | Does not support POST resolution of 2560x1080 | 5.5.1035 |
| [DCN] 3930413 | Updated few Vswing and Pre-emph Values for the DP and EDP | 5.5.1034 |
| [DCN] 3929778 [BUG] 5487660 | Output ports have blue screen | 5.5.1033 |
| [DCN] 3929433 | Update the DDI buffer translation entry for DP to fix the compliance issue. | 5.5.1032 |
| [DCN] [3929067] /[BUG] 5555060 /5528418 | Display blank-out and system hang observed when disabling or un-installing the IGFX driver. | 5.5.1031 |

| Feature addition, removal or enhancements | | |
|---|---|----------|
| Reference No. | Description | Build |
| [DCN] [3924434] / [BUG] 5528681 | Display blank out observed in eDP display | 5.5.1030 |
| [DCN] [3924226] | Correcting the delay for Idle pattern after Link training | 5.5.1030 |
| [DCN] [3924223] | Make T3 optimization to use HPD and T3 as software delay | 5.5.1030 |
| [DCN] [3923647] | New Customer Visible options in VBT in VBT are available for T14(PWM on -> backlight on) and T15 (backlight off -> PWM off). | 5.5.1030 |
| [DCN] [3923516] | Have a common VBT/GOP for both HSW and BDW without needing to select platform in BMP tool | 5.5.1029 |
| [DCN] [3922327] / [BUG] 5487294 | The system will halt with black screen if IGD memory set to 1024 MB. | 5.5.1028 |
| [DCN] [3922183] / [BUG] 5444527 | Garbage occurred when Set Mode during both legacy boot and DOS | 5.5.1027 |
| [DCN] [3921565] / [BUG] 5486512 | Updated some changes in Display Port low Voltage Swing /Pre-Emphasis values for eDP displays only. Note: 5.5.1026 is not released to External customers. | 5.5.1026 |
| [DCN] [3920799] | Added support for BDW for Voltage Swing Level0 and Pre-Emphasis Level3 | 5.5.1025 |
| [DCN] [3920760] | Change help text only for High/Low VswingPre Emphasis selection and some other corrections | 5.5.1025 |
| [DCN] [3919993] | Updated changes for Port DP/eDP link training | 5.5.1023 |
| [DCN] [3919908] | Code check for AUX divider WA and for number of read bytes received with AUX ACK | 5.5.1023 |
| [DCN] [3919654] [BUG] [5294396] | Vertical Retrace bit is not set in VGA register on VESA Video modes. | 5.5.1023 |
| [DCN] [3919285] | Program Voltage Swing and Pre-Emphasis tables and it's corresponding buffer translation values for eDP and DP as per the Broadwell spec | 5.5.1022 |
| [DCN] [3919208] | When we change T8 value of panel power sequencing to 1300, system fails to boot in legacy mode | 5.5.1022 |
| [DCN] [3918809] | Hang is seen with some Display Port monitors using Legacy VBIOS in POST | 5.5.1021 |
| [DCN] [3918638] | Black Screen after uninstall Intel Graphics driver with DP-HDMI dongle on LT2013P monitor. | 5.5.1021 |
| [DCN] [3918201] | Display blank-out observed in EDID less CRT connected through DP--> VGA dongle on HDW DT-Refresh. | 5.5.1021 |
| [DCN][3917678] | Fall Back to 8 BPC for HDMI/DVI if panel color depth from EDID 1.4 returned is 6 or 10 BPC | 5.5.1020 |
| [DCN][3917581] | Change the Display Port SSC enable/disable logic | 5.5.1020 |

| Feature addition, removal or enhancements | | |
|---|--|----------|
| Reference No. | Description | Build |
| [DCN][3917578] | System not booting past POST with 4K monitor attached | 5.5.1020 |
| [DCN][3917569] | System doesn't boot with DVI Display | 5.5.1020 |
| [DCN][3917396] | Hang is seen with some displayport monitors using Legacy Vbios in DOS | 5.5.1019 |
| [DCN][3916764] | Spell mistake on help text of mode removal table in VBT. | 5.5.1019 |
| [DCN][3915336] | BIOS setup screen not coming when booted in clone mode. | 5.5.1016 |
| [DCN] [3914984] | POST not observed on DP1.2 display in 4th Generation Intel® Core™ Processor Family based on Mobile U-Processor Line-Refresh system | 5.5.1015 |
| [DCN] [3881183] | [Bug ID: 5223417]eDP Display Brightness not changing with the Dobios 5f61 command | 5.5.1014 |
| [DCN] [3880618] | DTD fallback mechanism made similar to GOP driver code | 5.5.1013 |
| [DCN] [3879981] | Fixed the detection sequence logic for the DP->VGA dongle | 5.5.1013 |
| [DCN][3879974] | Updates to programming the panel color depth for all displays using EDID | 5.5.1013 |
| [DCN] [3879822] | Ubuntu* and Fedora* – fix porting from 4th Generation Intel® Core™ Processor Platform VBIOS version 2179 | 5.5.1013 |
| [DCN] [3879483] | Hide Windows* XP Related VBT Options in the BSF Script | 5.5.1011 |
| [DCN] [3879333] | Addition of the Device Id's for Broadwell for PCI 0 2 0 (Intel Gfx Device) device | 5.5.1010 |
| [DCN] [3879272] | VBIOS is using the incorrect bit mapping to retrieve the frame buffer size. | 5.5.1010 |
| [DCN] [3878871] | Increase LQA wait time 150ms to 200ms | 5.5.1010 |
| [DCN] [3878935] | VBIOS fails to detect for eDP panel to receive the HPD signal with glitch. | 5.5.1010 |
| [DCN] [3878661] | Post Signon messages need to be disabled by default. | 5.5.1010 |
| [DCN] [3878378] | Vbios fast link training, Cd clock correction logic and BSF bug fix related to T3 optimization | 5.5.1008 |
| [DCN] [3878360] | Ported the eDP power sequence changes from 4th Generation Intel® Core™ Processor Platform VBIOS to BDW VBIOS | 5.5.1008 |
| [DCN] [3878366] | Porting from 4th Generation Intel® Core™ Processor Platform VBIOS: eDP backlight control does not work correctly | 5.5.1008 |
| [DCN] [3875251] | Porting from 4th Generation Intel® Core™ Processor Platform VBIOS to BDW VBIOS: Assertive display technology enabled in VBT under Power Conservation features list for Windows driver configuration. | 5.5.1008 |
| [DCN] [3875255] | Bug Fix porting from 4th Generation Intel® Core™ Processor Platform - VBIOS will caused the black screen on CPT (panel vendor) sometimes. | 5.5.1008 |
| [DCN][3874413] | Support for CD clock , PCH raw clock and PCH stepping identification based on device ID's added | 5.5.1007 |
| [DCN][3874803] | LPT device id identification added in the BDW Vbios | 5.5.1007 |
| [DCN][3873885] | HDMI Level shifter list selection support (4th Generation Intel® Core™ Processor Platform or BDW) in VBT based on chipset selection | 5.5.1006 |
| [DCN][3873835] | Port DMRRS enabling feature from 4th Generation Intel® Core™ Processor Platform to BDW | 5.5.1006 |

Known Issues

| Known issues in Build 1039(Current Release) | | |
|---|-------------|----------------|
| Reference No. | Description | Affected OS(s) |
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