Erledigt DSDT für Asus H170M-Plus

Beitrag von „farbeweiß“ vom 2. Januar 2018, 00:20

Hallo und ein frohes neues Jahr zusammen,

ich probiere eine DSDT für oben genanntes Mainboard zu erstellen.
Ich orientiere mich dabei an den grandiosen Recourcen von @al6042:

https://www.youtube.com/watch?time_continue=325&v=4quIWFqlKlU
https://www.hackintosh-forum.d…php/FAQ/?category=20-ACPI (ich glaube das ist von ihm)

Leider komme ich am ende nicht bei einer gut funktionierenden DSDT heraus. Ich habe einige Verständnisfragen zu
den Tutorials und würde mich freuen wenn ihr mir weiterhelfen könntet.

Mein System:

Spoiler anzeigen

Ohne DSDT läuft das System ziemlich einwandfrei (sleep, sound, multi-monitor, speedstep, usb (nicht sicher)) mit folgenden Settings:

Config.plist und Kexte:

Spoiler anzeigen

Mein Ziel wäre es die Relevanten Einstellungen der config.plist in die DSDT aufzunehmen, sowie meine Geräte richtig in der DSDT zu definieren.
Mit meinen Versuchen bekomme ich am ende eine bootende DSDT ohne Sound (und vermutlich auch ohne Sleep), allerdings steht mit dieser unter "Systembericht -PCI" keine Tabelle mehr sondern ein einzelner Eintrag zu meiner GPU.

Für das Erstellen der DSDT habe ich mich an der Anleitung "3. Standard DSDT-Patch Verlauf für

Desktops Skylake-Chipsätze:" orientiert :
https://www.hackintosh-forum.d…php/FAQ/?category=20-ACPI
Vielleicht schreibe ich hier meine Anmerkungen und Fragen zur Anleitung und damit klärt sich schon alles:
1. Extrahiert mit CLover (f4)

zu Punkt 2 und 3: Ich habe in der DSDT jeweils HDAS-HDEF und HECI-IMEI "find&replaced"

zu Punkt 4
als annähernden Patch habe ich das H97M-E genommen und den Patch folgendermaßen angepasst (realtec Lan raus und "Intel Series 9" mit "Intel Series10" ersetzt:

Code

```
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# Last Updated : 05/07/2016
# Patch Name : H97M-E
# Patch Version : 1.01


#IASL:Check $BUILD = 20131218


#External Fixes
into_all all code_regex \/\*\sExternal\sreference\s\*\/\s+ removeall_matched;
into definitionblock code_regex ,\sUnknownObj removeall_matched;


into definitionblock code_regex External\s\(_SB_.PCI0.PEG0\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEG0.PEGP\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEG1\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEG2\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEGP\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEGP.GFX0\)\s+ removeall_matched;


```

```
# Add the DTGP method
into method label DTGP remove_entry;
into definitionblock code_regex . insert begin
Method (DTGP, 5, NotSerialized)\n
{\n
If (LEqual (Arg0, Buffer (0x10)\n
{\n
/* 0000 */ 0xC6, 0xB7, 0xB5, 0xA0, 0x18, 0x13, 0x1C, 0x44,\n
/* 0008 */ 0xB0, 0xC9, 0xFE, 0x69, 0x5E, 0xAF, 0x94, 0x9B\n
}))\n
{\n
If (LEqual (Arg1, One))\n
{\n
If (LEqual (Arg2, Zero))\n
{\n
Store (Buffer (One) { 0x03 }, Arg4)\n
Return (One)\n
}\n
If (LEqual (Arg2, One))\n
{\n
Return (One)\n
}\n
}\n
}\n
Store (Buffer (One) { 0x00 }, Arg4)\n
Return (Zero)\n
}
end;


# Add Darwin to the supported operating systems
into method label _INI code_regex (\s+)(If\s\(_OSI\s\("Windows\s2001"\)\)) replace_matched begin \n
If (_OSI ("Darwin"))\n
{\n
Store (0x2710, OSYS)\n
}\n
\n
$2
end;


```

```
# Intel 9 Series Fixes
into_all all code_regex _T_([0-6]) replaceall_matched begin T_%1 end;


into_all method label _INI code_regex INIR\n replace_matched begin Store (Zero, INIR)\n end;


into device label PCI0 code_regex (\s+Zero){2,} remove_matched;
into_all method label ADBG code_regex (?<=Return\s\(MDBG\))\s+Arg0 remove_matched;
into_all method label _PS3 code_regex PS3X\n replace_matched begin Store (Zero, PS3X)\n end;
into_all method label _PS2 code_regex PS2X\n replace_matched begin Store (Zero, PS2X)\n end;
into_all method label _PS0 code_regex PS0X\n replace_matched begin Store (Zero, PS0X)\n end;


into_all method label GPEH insert begin Return (Zero) end;


into method label _CRS parent_label PCI0 code_regex DWordField\s\(BUF0,\s\\_SB.PCI0._Y1E._LEN,\sMSLN\) replace_matched begin QWordField (BUF0, \\_SB.PCI0._Y1E._LEN, MSLN) end;


into_all all code_regex Acquire\s\(MUT0,\s0x0FFF\) replace_matched begin Acquire (MUT0, 0xFFFF) end;


into method label _HID parent_label _SB.PCI0.LPCB.TPM code_regex (Return\s\(Zero\)) remove_matched;
into method label _HID parent_label _SB.PCI0.LPCB.TPM insert begin Return (Zero) end;


into method label _PS0 parent_label NVM0 code_regex (Return\s\(Zero\)) remove_matched;
into method label _PS0 parent_label NVM0 insert begin Return (Zero) end;


into method label _PS0 parent_label XHC code_regex (Return\s\(Zero\)) remove_matched;
into method label _PS0 parent_label XHC insert begin Return (Zero) end;


```

```
into method label _PS0 parent_label RHUB code_regex (Return\s\(Zero\)) remove_matched;
into method label _PS0 parent_label RHUB insert begin Return (Zero) end;


into method label _PS3 parent_label NVM0 code_regex (Return\s\(Zero\)) remove_matched;
into method label _PS3 parent_label NVM0 insert begin Return (Zero) end;


into method label _PS3 parent_label XHC code_regex (Return\s\(Zero\)) remove_matched;
into method label _PS3 parent_label XHC insert begin Return (Zero) end;


into method label _DSM parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
into method label _DSM parent_label SHUB insert begin Return (Zero) end;


into method label RDCA parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
into method label RDCA insert begin Return (Zero) end;


into method label CNRS parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
into method label CNRS insert begin Return (Zero) end;


into method label SPL1 parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
into method label SPL1 insert begin Return (Zero) end;


into method label OE1X parent_label _GPE code_regex (Return\s\(Zero\)) remove_matched;
into method label OE1X parent_label _GPE insert begin Return (Zero) end;


# into method label TINI parent_label _SB.PCI0.RP05 code_regex (Return\s\(Zero\)) remove_matched;
into method label TINI parent_label _SB.PCI0.RP05 insert begin Return (Zero) end;


```

```
into method label TWAK parent_label _SB.PCI0.RP05 code_regex (Return\s\(Zero\)) remove_matched;
into method label TWAK parent_label _SB.PCI0.RP05 insert begin Return (Zero) end;


into method label TPTS parent_label _SB.PCI0.RP05 code_regex (Return\s\(Zero\)) remove_matched;
into method label TPTS parent_label _SB.PCI0.RP05 insert begin Return (Zero) end;


into method label SMSR parent_label _SB code_regex CreateDWordField\s\(Arg0,\s0x04,\sAEBX\)\s+ remove_matched;
into method label SDSP parent_label AMW0 code_regex CreateDWordField\s\(Arg0,\sZero,\sACTN\)\s+ remove_matched;
into method label GDSP parent_label AMW0 code_regex CreateDWordField\s\(Arg0,\sZero,\sACTN\)\s+ remove_matched;
into method label DEVS parent_label AMW0 code_regex CreateDWordField\s\(Arg0,\s0x04,\sCPAR\)\s+ remove_matched;


# Device injection for the Intel 9 Series SATA
into device label SAT0 set_label begin SATA end;
into_all all code_regex SAT0 replaceall_matched begin SATA end;
into device label SAT1 remove_entry;
into_all scope label _SB.PCI0.SAT0.PRT0 set_label begin _SB.PCI0.SATA.PRT0 end;
into_all scope label _SB.PCI0.SAT0.PRT1 set_label begin _SB.PCI0.SATA.PRT1 end;
into_all scope label _SB.PCI0.SAT0.PRT2 set_label begin _SB.PCI0.SATA.PRT2 end;
into_all scope label _SB.PCI0.SAT0.PRT3 set_label begin _SB.PCI0.SATA.PRT3 end;
into method label _DSM parent_label SATA remove_entry;
into device label SATA insert begin
Method (_DSM, 4, NotSerialized)\n
{\n
Store (Package (0x0A) {\n
"AAPL,slot-name", "Built In",\n
"name", "Intel AHCI Controller",\n
"model", Buffer (0x2D) {"Intel 10 Series Chipset Family SATA Controller"},\n
"device_type", Buffer (0x0F) {"AHCI Controller"},\n
"device-id", Buffer (0x04) {0x02,0x1E,0x00,0x00}\n
}, Local0)\n
DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))\n
Return (Local0)\n
}
end;
```

```
177.
178.
179. into method label SDSP parent_label AMW0 code_regex (Create) replace_matched begin //%1 end;
180. into method label GDSP parent_label AMW0 code_regex (Create) replace_matched begin //%1 end;
181. into method label SMSR parent_label _SB code_regex (CreateDWordField\s\(Arg0,\s0x04,\sAEBX\)) replace_matched begin //%1 end;
182. # Add the missing MCHC device
183. into device label MCHC parent_label PCI0 remove_entry;
184. into device label PCI0 insert begin
185. Device (MCHC)\n
186. {\n
187. Name (_ADR, Zero)\n
188. }
189. end;
190.
191.
192. # Fix the SMBUS to allow for loading of the AppleSMBusController.kext
193. into device label BUS0 parent_label SBUS remove_entry;
194. into device label SBUS insert begin
195. Device (BUS0)\n
196. {\n
197. Name (_CID, "smbus")\n
198. Name (_ADR, Zero)\n
199. Device (DVL0)\n
200. {\n
201. Name (_ADR, 0x57)\n
202. Name (_CID, "diagsvault")\n
203. Method (_DSM, 4, NotSerialized)\n
204. {\n
205. Store (Package (0x02) {\n
206. "address", 0x57
207. }, Local0)\n
208. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))\n
209. Return (Local0)\n
210. }\n
211. }\n
212. }
213. end;
214.
215.
216. # Patches the Intel USB3 on Intel 9 Series chipsets
```

```
into method label _DSM parent_label XHC remove_entry;
into device label XHC insert begin
Method (_DSM, 4, NotSerialized)\n
{\n
Store (Package (0x15) {\n
"AAPL,slot-name", "Built In",\n
"name", "Intel XHCI Controller",\n
"model", Buffer (0x37) {"Intel 10 Series Chipset Family USB xHCI Host Controller"},\n
"device_type", Buffer (0x0E) {"USB Controller"},\n
"AAPL,current-available", 0x0834,\n
"AAPL,current-extra", 0x0A8C,\n
"AAPL,current-in-sleep", 0x03E8,\n
"AAPL,current-extra-in-sleep", 0x0834,\n
"AAPL,max-port-current-in-sleep", 0x0A8C,\n
"AAPL,device-internal", 0x02,\n
Buffer (One) {0x00}\n
}, Local0)\n
DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))\n
Return (Local0)\n
}
end;
```

Alles anzeigen

Danach musste ich sat0 zu SATA umbenennen
Punkt 5,6,7,8 wie in der Anleitung
Danach habe ich den Patch für Z97X-UD7-TH angepasst um mein Intel Lan einzubauen (hier ist der fehler, oder ?)

Code

```
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# Last Updated : 05/07/2016
# Patch Name : Z97X-UD7-TH
# Patch Version : 1.0

```

```
#External Fixes
into_all all code_regex \/\*\sExternal\sreference\s\*\/\s+ removeall_matched;
into definitionblock code_regex ,\sUnknownObj removeall_matched;


into definitionblock code_regex External\s\(_SB_.PCI0.PEG0\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEG0.PEGP\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEG1\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEG2\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEGP\)\s+ removeall_matched;
into definitionblock code_regex External\s\(_SB_.PCI0.PEGP.GFX0\)\s+ removeall_matched;


# Intel 9 Series Fixes
into_all all code_regex _T_([0-6]) replaceall_matched begin T_%1 end;


into_all method label _INI code_regex INIR\n replace_matched begin Store (Zero, INIR)\n end;


into device label PCI0 code_regex (\s+Zero){2,} remove_matched;
into_all method label ADBG code_regex (?<=Return\s\(MDBG\))\s+Arg0 remove_matched;
into_all method label _PS3 code_regex PS3X\n replace_matched begin Store (Zero, PS3X)\n end;
into_all method label _PS2 code_regex PS2X\n replace_matched begin Store (Zero, PS2X)\n end;
into_all method label _PS0 code_regex PS0X\n replace_matched begin Store (Zero, PS0X)\n end;


into_all method label GPEH insert begin Return (Zero) end;


into method label _CRS parent_label PCI0 code_regex DWordField\s\(BUF0,\s\\_SB.PCI0._Y1E._LEN,\sMSLN\) replace_matched begin QWordField (BUF0, \\_SB.PCI0._Y1E._LEN, MSLN) end;


into_all all code_regex Acquire\s\(MUT0,\s0x0FFF\) replace_matched begin Acquire (MUT0, 0xFFFF) end;

```

```
49.
50. into method label _HID parent_label _SB.PCI0.LPCB.TPM code_regex (Return\s\(Zero\)) remove_matched;
51. into method label _HID parent_label _SB.PCI0.LPCB.TPM insert begin Return (Zero) end;
52.
53.
54. into method label _PS0 parent_label NVM0 code_regex (Return\s\(Zero\)) remove_matched;
55. into method label _PS0 parent_label NVM0 insert begin Return (Zero) end;
56.
57.
58. into method label _PS0 parent_label XHC code_regex (Return\s\(Zero\)) remove_matched;
59. into method label _PS0 parent_label XHC insert begin Return (Zero) end;
60.
61.
62. into method label _PS0 parent_label RHUB code_regex (Return\s\(Zero\)) remove_matched;
63. into method label _PS0 parent_label RHUB insert begin Return (Zero) end;
64.
65.
66. into method label _PS3 parent_label NVM0 code_regex (Return\s\(Zero\)) remove_matched;
67. into method label _PS3 parent_label NVM0 insert begin Return (Zero) end;
68.
69.
70. into method label _PS3 parent_label XHC code_regex (Return\s\(Zero\)) remove_matched;
71. into method label _PS3 parent_label XHC insert begin Return (Zero) end;
72.
73.
74. into method label _DSM parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
75. into method label _DSM parent_label SHUB insert begin Return (Zero) end;
76.
77.
78. into method label RDCA parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
79. into method label RDCA insert begin Return (Zero) end;
80.
81.
82. into method label CNRS parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
83. into method label CNRS insert begin Return (Zero) end;
84.
85.
```

```
into method label SPL1 parent_label SHUB code_regex (Return\s\(Zero\)) remove_matched;
into method label SPL1 insert begin Return (Zero) end;


into method label OE1X parent_label _GPE code_regex (Return\s\(Zero\)) remove_matched;
into method label OE1X parent_label _GPE insert begin Return (Zero) end;


# into method label TINI parent_label _SB.PCI0.RP05 code_regex (Return\s\(Zero\)) remove_matched;
into method label TINI parent_label _SB.PCI0.RP05 insert begin Return (Zero) end;


into method label TWAK parent_label _SB.PCI0.RP05 code_regex (Return\s\(Zero\)) remove_matched;
into method label TWAK parent_label _SB.PCI0.RP05 insert begin Return (Zero) end;


into method label TPTS parent_label _SB.PCI0.RP05 code_regex (Return\s\(Zero\)) remove_matched;
into method label TPTS parent_label _SB.PCI0.RP05 insert begin Return (Zero) end;


into method label SMSR parent_label _SB code_regex CreateDWordField\s\(Arg0,\s0x04,\sAEBX\)\s+ remove_matched;
into method label SDSP parent_label AMW0 code_regex CreateDWordField\s\(Arg0,\sZero,\sACTN\)\s+ remove_matched;
into method label GDSP parent_label AMW0 code_regex CreateDWordField\s\(Arg0,\sZero,\sACTN\)\s+ remove_matched;
into method label DEVS parent_label AMW0 code_regex CreateDWordField\s\(Arg0,\s0x04,\sCPAR\)\s+ remove_matched;


# Fixes 'Built-in' status for Intel Gigabit Ethernet
into method label _DSM parent_label GLAN remove_entry;
into device label GLAN insert begin
Method (_DSM, 4, NotSerialized)\n
{\n
Store (Package (0x0C) {\n
"AAPL,slot-name", "Built In",\n
"name", "Intel Ethernet Controller",\n
"model", "Intel I217-V Gigabit Network Controller",\n
```

```
121. "device_type", Buffer (0x13) {"Ethernet Controller"},\n
122. "built-in", Buffer (One) {0x01},\n
123. "location", Buffer (0x02) {"1"}\n
124. }, Local0)\n
125. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))\n
126. Return (Local0)\n
127. }
128. end;
```

Alles anzeigen

Den Realtec Audio eintrag habe ich von den Toleda Patches angepasst (und die layout ID auf 7 gesetzt, damit es die
funktionierende Einstellung - audio layout id for AppleALC - aus der config.plist ersetzt)

die anderen Schritte waren wieder wie in der Anleitung.
Ich muss sagen ich kapiere grob, was ich da mache mit der DSDT, ich kapiere aber vor allem, dass ich ohne eure Hilfe aufgeschmissen bin und wäre euch darum sehr dankbar.
Bitte weißt mich darauf hin wenn mein Betrag falsch Formatiert oder ähnliches ist.

[/b]