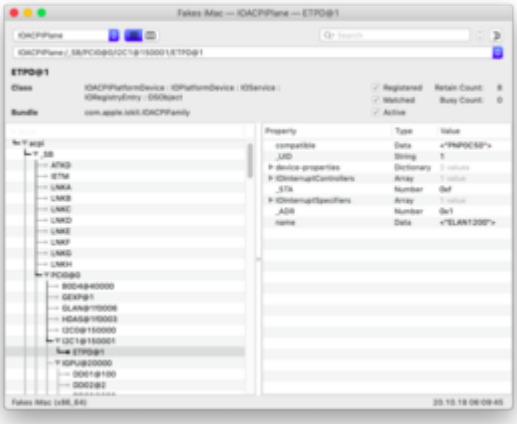
Erledigt Probleme mit ELAN1200

Beitrag von „fake“ vom 21. Oktober 2018, 14:31

Ich bekomm es nicht hin. Kann mir einer von euch beim Patchen helfen?



ich würde gern folgende patch anwenden, beim ersten kommt dann 42360, 6088, Object is not accessible from this scope (SBFB)

Code

```
1. # GPIO Pinning for ELAN1200 by HackinDoge
2. # Confirmed working on ASUS FX502VM-AH51
3.
4. into device label ETPD insert begin
5.
6. Name (SBFG, ResourceTemplate ()\n
7. {\n
8. GpioInt (Level, ActiveLow, ExclusiveAndWake, PullDefault, 0x0000,\n
9. "\\_SB.PCI0.GPI0", 0x00, ResourceConsumer, ,\n
10. )\n
11. { // Pin list\n
12. 0x47\n
13. }\n
14. })\n
15.
16. end;
17.
```

18. into method label _CRS parent_label ETPD replace_content begin
19.
20. Return (ConcatenateResTemplate (SBFB, SBFG))
21.
22. end;

Alles anzeigen

und bei zweiten

15503, 6126, syntax error, unexpected PARSEOP_SCOPE, expecting $end and premature End-Of-File

Code

1. # Skylake controller patches for VoodooI2C
2. # Ensures that VoodooI2C can interface with I2C Controllers
3. # Written and maintained by Alexandre Daoud
4.
5. into_all device label I2C0 remove_entry;
6. into_all device label I2C1 remove_entry;
7. into_all device label I2C2 remove_entry;
8. into_all device label I2C3 remove_entry;
9. into_all device label I2C4 remove_entry;
10. into_all device label I2C5 remove_entry;
11.
12. into scope label _SB.PCI0 insert begin
13.
14. Device (I2C0)\n
15. {\n
16. Name (LINK, "_SB.PCI0.I2C0")\n
17. Method (_PSC, 0, NotSerialized) // _PSC: Power State Current\n
18. {\n
19. Return (GETD (SB10))\n
20. }\n
21. \n
22. Method (_PS0, 0, NotSerialized) // _PS0: Power State 0\n
23. {\n
24. LPD0 (SB10)\n
25. }\n
26. \n
27. Method (_PS3, 0, NotSerialized) // _PS3: Power State 3\n
28. {\n

29. LPD3 (SB10)\n
30. }\n
31. }\n
32. \n\n
33. Device (I2C1)\n
34. {\n
35. Name (LINK, "_SB.PCI0.I2C1")\n
36. Method (_PSC, 0, NotSerialized) // _PSC: Power State Current\n
37. {\n
38. Return (GETD (SB11))\n
39. }\n
40. \n
41. Method (_PS0, 0, NotSerialized) // _PS0: Power State 0\n
42. {\n
43. LPD0 (SB11)\n
44. }\n
45. \n
46. Method (_PS3, 0, NotSerialized) // _PS3: Power State 3\n
47. {\n
48. LPD3 (SB11)\n
49. }\n
50. }\n
51. \n\n
52. Device (I2C2)\n
53. {\n
54. Name (LINK, "_SB.PCI0.I2C2")\n
55. Method (_PSC, 0, NotSerialized) // _PSC: Power State Current\n
56. {\n
57. Return (GETD (SB12))\n
58. }\n
59. \n
60. Method (_PS0, 0, NotSerialized) // _PS0: Power State 0\n
61. {\n
62. LPD0 (SB12)\n
63. }\n
64. \n
65. Method (_PS3, 0, NotSerialized) // _PS3: Power State 3\n
66. {\n
67. LPD3 (SB12)\n
68. }\n
69. }\n
70. \n\n

```
Device (I2C3)\n
{\n
Name (LINK, "\\_SB.PCI0.I2C3")\n
Method (_PSC, 0, NotSerialized) // _PSC: Power State Current\n
{\n
Return (GETD (SB13))\n
}\n
\n
Method (_PS0, 0, NotSerialized) // _PS0: Power State 0\n
{\n
LPD0 (SB13)\n
}\n
\n
Method (_PS3, 0, NotSerialized) // _PS3: Power State 3\n
{\n
LPD3 (SB13)\n
}\n
}\n
\n
Device (I2C4)\n
{\n
Name (LINK, "\\_SB.PCI0.I2C4")\n
Method (_PSC, 0, NotSerialized) // _PSC: Power State Current\n
{\n
Return (GETD (SB14))\n
}\n
\n
Method (_PS0, 0, NotSerialized) // _PS0: Power State 0\n
{\n
LPD0 (SB14)\n
}\n
\n
Method (_PS3, 0, NotSerialized) // _PS3: Power State 3\n
{\n
LPD3 (SB14)\n
}\n
}\n
\n
Device (I2C5)\n
{\n
Name (LINK, "\\_SB.PCI0.I2C5")\n
Method (_PSC, 0, NotSerialized) // _PSC: Power State Current\n
```

```
{\n
Return (GETD (SB15))\n
}\n
\n
Method (_PS0, 0, NotSerialized) // _PS0: Power State 0\n
{\n
LPD0 (SB15)\n
}\n
\n
Method (_PS3, 0, NotSerialized) // _PS3: Power State 3\n
{\n
LPD3 (SB15)\n
}\n
}\n
\n
end;

into scope label _SB.PCI0.I2C0 replace_content begin

Name (_HID, "INT3442") // _HID: Hardware ID\n
Method (_HRV, 0, NotSerialized) // _HRV: Hardware Revision\n
{\n
Return (LHRV (SB10))\n
}\n
\n
Method (_CRS, 0, NotSerialized) // _CRS: Current Resource Settings\n
{\n
Return (LCRS (SMD0, SB00, SIR0))\n
}\n
\n
Method (_STA, 0, NotSerialized) // _STA: Status\n
{\n
Return (LSTA (SMD0))\n
}\n
}\n
end;

into scope label _SB.PCI0.I2C1 replace_content begin

Name (_HID, "INT3443") // _HID: Hardware ID\n
Method (_HRV, 0, NotSerialized) // _HRV: Hardware Revision\n
{\n
Return (LHRV (SB11))\n
```

```
}\n
\n
Method (_CRS, 0, NotSerialized) // _CRS: Current Resource Settings\n
{\n
Return (LCRS (SMD1, SB01, SIR1))\n
}\n
\n
Method (_STA, 0, NotSerialized) // _STA: Status\n
{\n
Return (LSTA (SMD1))\n
}\n
}\n
end;

into scope label _SB.PCI0.I2C2 replace_content begin

Name (_HID, "INT3444") // _HID: Hardware ID\n
Method (_HRV, 0, NotSerialized) // _HRV: Hardware Revision\n
{\n
Return (LHRV (SB12))\n
}\n
\n
Method (_CRS, 0, NotSerialized) // _CRS: Current Resource Settings\n
{\n
Return (LCRS (SMD2, SB02, SIR2))\n
}\n
\n
Method (_STA, 0, NotSerialized) // _STA: Status\n
{\n
Return (LSTA (SMD2))\n
}\n
}\n
end;
into scope label _SB.PCI0.I2C3 replace_content begin

Name (_HID, "INT3445") // _HID: Hardware ID\n
Method (_HRV, 0, NotSerialized) // _HRV: Hardware Revision\n
{\n
Return (LHRV (SB13))\n
}\n
\n
Method (_CRS, 0, NotSerialized) // _CRS: Current Resource Settings\n
{\n
```

```
Return (LCRS (SMD3, SB03, SIR3))\n
}\n
\n
Method (_STA, 0, NotSerialized) // _STA: Status\n
{\n
Return (LSTA (SMD3))\n
}\n
}\n
end;

into scope label _SB.PCI0.I2C4 replace_content begin

Name (_HID, "INT3446") // _HID: Hardware ID\n
Method (_HRV, 0, NotSerialized) // _HRV: Hardware Revision\n
{\n
Return (LHRV (SB14))\n
}\n
\n
Method (_CRS, 0, NotSerialized) // _CRS: Current Resource Settings\n
{\n
Return (LCRS (SMD4, SB04, SIR4))\n
}\n
\n
Method (_STA, 0, NotSerialized) // _STA: Status\n
{\n
Return (LSTA (SMD4))\n
}\n
}\n
end;

into scope label _SB.PCI0.I2C5 replace_content begin

Name (_HID, "INT3447") // _HID: Hardware ID\n
Method (_HRV, 0, NotSerialized) // _HRV: Hardware Revision\n
{\n
Return (LHRV (SB15))\n
}\n
\n
Method (_CRS, 0, NotSerialized) // _CRS: Current Resource Settings\n
{\n
Return (LCRS (SMD5, SB05, SIR5))\n
}\n
\n
```

```
242.  Method (_STA, 0, NotSerialized) // _STA: Status\n
243.  {\n
244.  Return (LSTA (SMD5))\n
245.  }\n
246.  }\n
247.  end;
```

Alles anzeigen

ich verstehe nicht warum das nicht geht.