# Intel NUC - Bean Canyon - NUC8i7BEH

## Beitrag von „rubenszy“ vom 29. November 2018, 06:23

ssdtPRGen brauch zwei Sachen einmal CPU Daten

und dann noch SMBios Daten

Solltest du beides nicht haben, musst du halt per Hand eine SSDT erstellen

```
Usage: ./ssdtPRGen.sh [-abcdefghiklmnoprsutvwx]
       -acpi  Processor name (example: CPU0, C000)
       -acpi  Processor name (example: CPU0, C000)
       -bclk  Frequency (base clock frequency)
       -board-id (example: Mac-F60DEB81FF30ACF6)
       -cpus number of physical processors [1-4]
       -debug output [0-3]
          0 = no debug injection/debug output
          1 = inject debug statements in: ssdt_pr.dsl
          2 = show debug output
          3 = both
       -developer mode [0-1]
          0 = disabled - Use files from: /Users/[username]/Library/ssdtPRGen
          1 = enabled  - Use files from: /Users/[username]/Projects/ssdtPRGen.sh
       -extract ACPI tables to [target path]
       -frequency (clock frequency)
       -help info (this)
       -lfreq=, lowest idle frequency
       -logical processors [2-128]
       -mode script mode [normal/custom]:
          normal - Use ACPI/DSMOS data from the host computer
          custom - Use ACPI data from: /Users/[username]/Desktop
          ..                           : /Users/[username]/Desktop
       -model (example: MacPro6,1)
       -open the previously generated SSDT
       -processor model (example: 'E3-1285L v3')
       -show supported board-id and model combinations:
          Sandy Bridge
          Ivy Bridge
          Haswell
          Broadwell
          Skylake
          KabyLake
       -target CPU type:
          0 = Sandy Bridge
          1 = Ivy Bridge
          2 = Haswell
          3 = Broadwell
          4 = Skylake
          5 = KabyLake
       -turbo maximum (turbo) frequency:
          6300 for Sandy Bridge and Ivy Bridge
          8000 for Haswell, Broadwell and greater
       -tdp [51.5 - 1540]
       -compatibility workarounds:
          0 = no workarounds
          1 = inject extra (turbo) P-State at the top with maximum (turbo) frequency + 1 MHz
          2 = inject extra P-States at the bottom
          3 = both
       -xcpm mode:
          0 = XCPM mode disabled
          1 = XCPM mode enabled
```