

# Dell Vostro 5370

Beitrag von „Harper Lewis“ vom 29. Dezember 2018, 11:18

Moin,

in der [Anleitung zum Thema GPIO pinning](#) ist folgendes zu lesen:

Zitat

This `Name` may appear in the root level of your device entry or it could possibly appear in the `_CRS` method. In the first case we shall say that your device is **root pinned**. In the second case, we shall say that your device is **CRS pinned**. **If, furthermore, the numbers that appear under pin list are non-zero** then we shall furthermore append **well-** to the previous names as follows: **well-root pinned** and **well-CRS pinned**. We shall just say **well-pinned** to mean either of these latter cases. If you cannot find such a `Name` then we shall say that your device is **unpinned**.

Und so schaut das in der DSDT aus (`_SB.PCI0.I2C0 > TPD0`):

Code

1. Name (SBFG, ResourceTemplate ())
2. {
3. GpioInt (Level, ActiveLow, Exclusive, PullUp, 0x0000,
4. "\\\_SB.PCI0.GPIO", 0x00, ResourceConsumer, ,
5. )
6. { // Pin list
7. 0x001B
8. }
9. })