

HowTo: Thunderbolt HotPlug/HotSwap Finetuning für euren Hackintosh

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[kuckkuck](#)

Dann so 😊

Code

```
1. DefinitionBlock ("", "SSDT", 2, "HACKI", "TBOLT3", 0x00000000)
2. {
3.   External (_SB_.PCI0.RP01, DeviceObj)
4.   External (_SB_.PCI0.RP01.PXSX, DeviceObj)
5.   External (DTGP, MethodObj) // 5 Arguments
6.
7.   Scope (_SB.PCI0.RP01)
8.   {
9.     If (_OSI ("Darwin"))
10.    {
11.      Scope (PXSX)
12.      {
13.        Name (_STA, Zero) // _STA: Status
14.      }
15.
16.      Device (UPSB)
17.      {
18.        Name (_ADR, Zero) // _ADR: Address
19.        Method (_STA, 0, NotSerialized) // _STA: Status
20.        {
21.          Return (0x0F)
22.        }
23.
24.        Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method
25.        {
26.          Local0 = Package (0x02)
27.          {
28.            "PCI-Thunderbolt",
29.            One
30.          }

```

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31. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
32. Return (Local0)
33. }
34.
35. Device (DSB0)
36. {
37. Name (_ADR, Zero) // _ADR: Address
38. Method (_STA, 0, NotSerialized) // _STA: Status
39. {
40. Return (0x0F)
41. }
42.
43. Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method
44. {
45. Local0 = Package (0x04)
46. {
47. "AAPL,slot-name",
48. Buffer (0x09)
49. {
50. "Built In"
51. },
52.
53. "PCIHotplugCapable",
54. One
55. }
56. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
57. Return (Local0)
58. }
59.
60. Device (NH10)
61. {
62. Name (_ADR, Zero) // _ADR: Address
63. Name (_STR, Unicode ("Thunderbolt")) // _STR: Description String
64. Method (_STA, 0, NotSerialized) // _STA: Status
65. {
66. Return (0x0F)
67. }
68.
69. Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method
70. {
71. Local0 = Package (0x0B)
72. {
73. "AAPL,slot-name",
74. Buffer (0x07)

```

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75. {
76. "Slot-3"
77. },
78.
79. "device_type",
80. Buffer (0x19)
81. {
82. "Thunderbolt 3-Controller"
83. },
84.
85. "model",
86. Buffer (0x2C)
87. {
88. "Intel JHL7540 Titan Ridge Thunderbolt 3 NHI"
89. },
90.
91. "name",
92. Buffer (0x23)
93. {
94. "Titan Ridge Thunderbolt Controller"
95. },
96.
97. "power-save",
98. One,
99. Buffer (One)
100. {
101. 0x00 // .
102. }
103. }
104. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
105. Return (Local0)
106. }
107. }
108. }
109.
110. Device (DSB1)
111. {
112. Name (_ADR, 0x00010000) // _ADR: Address
113. Method (_STA, 0, NotSerialized) // _STA: Status
114. {
115. Return (0x0F)
116. }
117.
118. Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method

```

```
119. {
120. Local0 = Package (0x04)
121. {
122. "AAPL,slot-name",
123. Buffer (0x09)
124. {
125. "Built In"
126. },
127.
128. "PCIHotplugCapable",
129. One
130. }
131. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
132. Return (Local0)
133. }
134. }
135.
136. Device (DSB2)
137. {
138. Name (_ADR, 0x00020000) // _ADR: Address
139. Method (_STA, 0, NotSerialized) // _STA: Status
140. {
141. Return (0x0F)
142. }
143.
144. Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method
145. {
146. Local0 = Package (0x04)
147. {
148. "AAPL,slot-name",
149. Buffer (0x09)
150. {
151. "Built In"
152. },
153.
154. "PCIHotplugCapable",
155. One
156. }
157. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
158. Return (Local0)
159. }
160.
161. Device (XHC5)
162. {
```

```
163. Name (_ADR, Zero) // _ADR: Address
164. Method (_STA, 0, NotSerialized) // _STA: Status
165. {
166. Return (0x0F)
167. }
168.
169. Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method
170. {
171. Local0 = Package (0x10)
172. {
173. "AAPL,slot-name",
174. Buffer (0x07)
175. {
176. "Slot-3"
177. },
178.
179. "model",
180. Buffer (0x22)
181. {
182. "Intel JHL7540 Titan Ridge USB 3.1"
183. },
184.
185. "name",
186. Buffer (0x1F)
187. {
188. "Titan Ridge USB 3.1 Controller"
189. },
190.
191. "USBBusNumber",
192. Zero,
193. "UsbCompanionControllerPresent",
194. One,
195. "AAPL,XHCI-clock-id",
196. One,
197. "IOPCIExpressCapabilites",
198. 0x02,
199. "IOPCIHPTYPE",
200. 0x02
201. }
202. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
203. Return (Local0)
204. }
205. }
206. }
```

```
207.
208. Device (DSB4)
209. {
210. Name (_ADR, 0x00040000) // _ADR: Address
211. Method (_STA, 0, NotSerialized) // _STA: Status
212. {
213. Return (0x0F)
214. }
215.
216. Method (_DSM, 4, NotSerialized) // _DSM: Device-Specific Method
217. {
218. Local0 = Package (0x04)
219. {
220. "AAPL,slot-name",
221. Buffer (0x09)
222. {
223. "Built In"
224. },
225.
226. "PCIHotplugCapable",
227. One
228. }
229. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
230. Return (Local0)
231. }
232. }
233. }
234. }
235. Else
236. {
237. }
238. }
239. }
```

Alles anzeigen