Erledigt **Natives NVRAM auf dem Z390, Durchbruch?**

Beitrag von „apfelnico" vom 20. Januar 2020, 14:05

> Zitat von vit9696
>
> PMC device has nothing to do to LPC bus, but is added to its scope for faster initialisation. If we add it to PCI0, where it normally exists, it will start in the end of PCI configuration, which is too late for NVRAM support.

Diesen "Trick" finde ich interessant. 🙂