

i9 10900k / Gigabyte Z490 Vision D - Er läuft

Beitrag von „SchmockLord“ vom 6. Juni 2020, 11:31

Servus zusammen,

ich hab jetzt auf meinem das Problem, was ich eigentlich mit jedem Hackintosh irgendwann mal hatte: Sleep/Wake Issues.

Ich hab eigentlich alles probiert, was mir einfällt:

- USB-Ports sind imho sauber konfiguriert
- darkwake=0
- TCPKEEPALIVE 0
- hibernatemode 0
- proximitywake 0
- powernap 0
- sleepimage gelöscht

Aber er wacht immer wieder aus dem Sleep auf.

Erst hatte ihn das tcpkeepalive immer wieder aufgeweckt. Dann hab ich es auf 0 gesetzt.

Jetzt wacht er mit dem Wake reason auf:

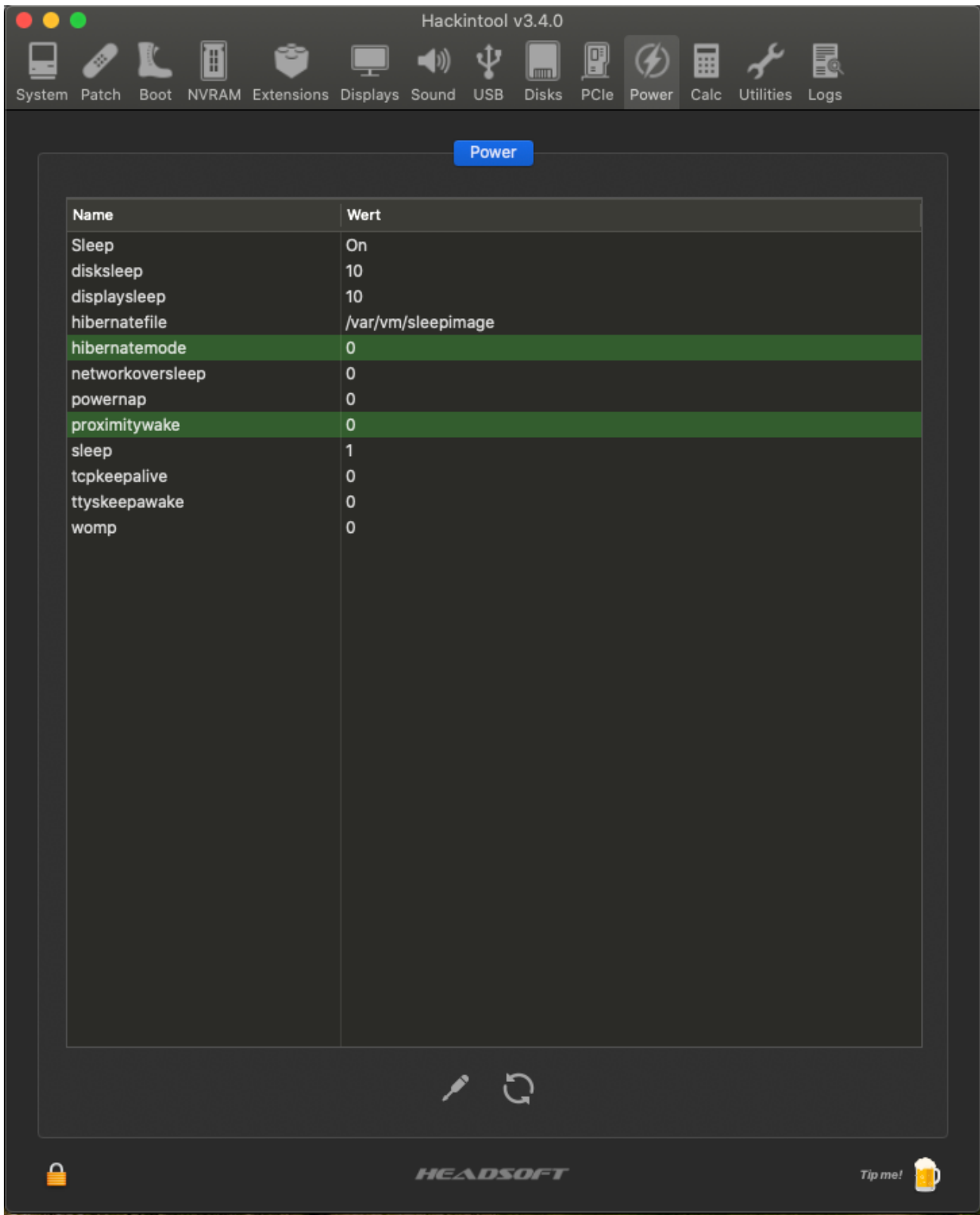
Wake from Normal Sleep [CDNVA] : due to XDCI CNVW PEG2 RP04/UserActivity Assertion Using AC (Charge:0%)

Im IORegistryExplorer hab ich aber überhaupt kein RP04.

Könnt ihr mir helfen?

Hier mal mein Log zu Sleep/Wake:

[Sleep:Wake reasons.pdf](#)



Hackintool v3.4.0

System Patch Boot NVRAM Extensions Displays Sound **USB** Disks PCIe Power Calc Utilities Logs







USB

Type	ID	Name	Series	Herstell...	Device ID
XHC	0x14	Comet Lake USB 3.1 xHCI Host Controller	Unknown	0x8086	0x06ED
XHC5	0x00	JHL7540 Thunderbolt 3 USB Controller [Titan...	Unknown	0x8086	0x15EC


USB Ports

Type	Name	Location ID	Port	Connector	Dev Speed	Device	Comment
XHC	HS03	0x14100000	0x03	USB3	480 Mbps	USB2.0 Hub	
XHC	HS04	0x14200000	0x04	USB3	Unknown		
XHC	HS05	0x14300000	0x05	USB3	Unknown		
XHC	HS06	0x14400000	0x06	USB3	Unknown		
XHC	HS07	0x14500000	0x07	USB3	480 Mbps	4-Port USB 2.1 Hub	
XHC	HS09	0x14600000	0x09	USB2	12 Mbps	USB-HID Keyboard	
XHC	HS11	0x14700000	0x0B	USB2	480 Mbps	USB2.0 Hub	
XHC	HS12	0x14800000	0x0C	USB2	12 Mbps	ITE Device	
XHC	SS01	0x14900000	0x11	TypeC+Sw	Unknown		
XHC	SS02	0x14A00000	0x12	TypeC+Sw	Unknown		
XHC	SS03	0x14B00000	0x13	USB3	5 Gbps	USB3.0 Hub	
XHC	SS04	0x14C00000	0x14	USB3	Unknown		
XHC	SS05	0x14D00000	0x15	USB3	5 Gbps	Ultra	
XHC	SS06	0x14E00000	0x16	USB3	Unknown		
XHC	SS07	0x14F00000	0x17	USB3	5 Gbps	4-Port USB 3.1 Hub	
XHC5	SSP1	0x00100000	0x03	TypeC+Sw	Unknown		
XHC5	SSP2	0x00200000	0x04	TypeC+Sw	Unknown		

● Active Port

HEADSOFT

Tip me! 

pci1442.000 Hackintosh v3.4.0

System Patch Boot NVRAM Extensions Displays Sound USB Disk PCIe Power LK0 USBs Logs

PCI

Device	Vendor	Sub-Device	ASPM	Vendor Name	Device Name	Class	Subclass	Chip Name	Chip IDName	Device Path
00:02.0	0x8086	0x0933	0x0200	Disabled	Intel Corporation	Bridge	Host bridge			PciRoot(0x0)/Pci(0x0/0x0)
00:01.0	0x8086	0x1901	0x0040	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:0:0:0	pci8086,8333	PciRoot(0x0)/Pci(0x0/0x1/0x0)
00:01.1	0x8086	0x1902	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:0:0:1	pci8086,8333	PciRoot(0x0)/Pci(0x0/0x1/0x1)
00:12.0	0x8086	0x0938	0x0480	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:12:0:0	pci8086,8333	PciRoot(0x0)/Pci(0x0/0x12/0x0)
00:14.0	0x8086	0x06ED	0x1458	Disabled	Intel Corporation	Serial bus controller	Serial bus controller	PCIO@0:14:0:0	pci8086,6ed	PciRoot(0x0)/Pci(0x0/0x14/0x0)
00:14.2	0x8086	0x06E7	0x1458	Disabled	Intel Corporation	Memory controller	RAM memory	PCIO@0:14:2:0	pci8086,6e7	PciRoot(0x0)/Pci(0x0/0x14/0x2)
00:18.0	0x8086	0x06ED	0x1458	Disabled	Intel Corporation	Communication controller	Communication controller	PCIO@0:18:0:0	pci8086,6ed	PciRoot(0x0)/Pci(0x0/0x18/0x0)
01:00.0	0x1002	0x1440	0x0040	Disabled	Advanced Micro Devices, Inc. [A...]	Bridge	PCI bridge	PCIO@0:1:0:0	pci1002,1440	PciRoot(0x0)/Pci(0x0/0x01/0x0)
00:17.0	0x8086	0x0202	0x1458	Disabled	Intel Corporation	Mass storage controller	SATA controller	PCIO@0:17:0:0	pci8086,202	PciRoot(0x0)/Pci(0x0/0x17/0x0)
04:00.0	0x14E4	0x43A0	0x0080	0x0111	Libo-L1	Network controller	Network controller	PCIO@0:4:0:0	pci14e4,43a0	PciRoot(0x0)/Pci(0x0/0x04/0x0)
00:1B.0	0x8086	0x09C0	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1B:0:0	pci8086,9c0	PciRoot(0x0)/Pci(0x0/0x1B/0x0)
00:1E.4	0x8086	0x05A0	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1E:4:0	pci8086,5a0	PciRoot(0x0)/Pci(0x0/0x1E/0x4)
00:1C.0	0x8086	0x0689	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1C:0:0	pci8086,689	PciRoot(0x0)/Pci(0x0/0x1C/0x0)
00:1C.1	0x8086	0x0689	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1C:1:0	pci8086,689	PciRoot(0x0)/Pci(0x0/0x1C/0x1)
00:1C.4	0x8086	0x0689	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1C:4:0	pci8086,689	PciRoot(0x0)/Pci(0x0/0x1C/0x4)
00:1D.0	0x8086	0x0689	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1D:0:0	pci8086,689	PciRoot(0x0)/Pci(0x0/0x1D/0x0)
00:1D.4	0x8086	0x0689	0x0000	Disabled	Intel Corporation	Bridge	PCI bridge	PCIO@0:1D:4:0	pci8086,689	PciRoot(0x0)/Pci(0x0/0x1D/0x4)
00:1F.0	0x8086	0x0689	0x0000	Disabled	Intel Corporation	Bridge	ISA bridge	PCIO@0:1F:0:0	pci8086,689	PciRoot(0x0)/Pci(0x0/0x1F/0x0)
00:1F.3	0x8086	0x0A70	0x0020	Disabled	Intel Corporation	Multimedia controller	Audio device	PCIO@0:1F:3:0	pci8086,a70	PciRoot(0x0)/Pci(0x0/0x1F/0x3)
00:1F.4	0x8086	0x06A3	0x1458	0x0001	Intel Corporation	Serial bus controller	SMBus	PCIO@0:1F:4:0	pci8086,6a3	PciRoot(0x0)/Pci(0x0/0x1F/0x4)
00:1F.5	0x8086	0x06A3	0x1458	0x0000	Intel Corporation	Serial bus controller	Serial bus controller	PCIO@0:1F:5:0	pci8086,6a3	PciRoot(0x0)/Pci(0x0/0x1F/0x5)
00:1F.6	0x8086	0x0A40	0x1458	0x0000	Intel Corporation	Network controller	Ethernet controller	PCIO@0:1F:6:0	pci8086,a40	PciRoot(0x0)/Pci(0x0/0x1F/0x6)
02:00.0	0x1002	0x1441	0x0040	Disabled	Advanced Micro Devices, Inc. [A...]	Bridge	PCI bridge	PCIO@0:2:0:0	pci1002,1441	PciRoot(0x0)/Pci(0x0/0x02/0x0)
42:00.0	0x1440	0x4920	0x1440	Disabled	Samsung Electronics Co Ltd	Mass storage controller	Non-Volatile memory controller	PCIO@0:42:0:0	pci1440,4920	PciRoot(0x0)/Pci(0x0/0x42/0x0)
08:00.0	0x8086	0x15F2	0x1458	0x0000	Intel Corporation	Network controller	Ethernet controller	PCIO@0:8:0:0	pci8086,15f2	PciRoot(0x0)/Pci(0x0/0x08/0x0)
03:00.0	0x1002	0x1441	0x0000	Disabled	Advanced Micro Devices, Inc. [A...]	Display controller	VGA compatible controller	PCIO@0:3:0:0	pci1002,1441	PciRoot(0x0)/Pci(0x0/0x03/0x0)
03:00.1	0x1002	0x1441	0x0000	Disabled	Advanced Micro Devices, Inc. [A...]	Display controller	VGA compatible controller	PCIO@0:3:0:1	pci1002,1441	PciRoot(0x0)/Pci(0x0/0x03/0x1)
03:00.2	0x1002	0x1441	0x0000	Disabled	Advanced Micro Devices, Inc. [A...]	Display controller	VGA compatible controller	PCIO@0:3:0:2	pci1002,1441	PciRoot(0x0)/Pci(0x0/0x03/0x2)
06:00.0	0x8086	0x1440	0x0000	Disabled	Intel Corporation	Mass storage controller	Non-Volatile memory controller	PCIO@0:6:0:0	pci8086,1440	PciRoot(0x0)/Pci(0x0/0x06/0x0)
43:00.0	0x8086	0x15E4	0x0020	Disabled	Intel Corporation	Mass storage controller	Non-Volatile memory controller	PCIO@0:43:0:0	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x43/0x0)
09:00.0	0x1440	0x4904	0x1440	0x0000	Disabled	Samsung Electronics Co Ltd	Mass storage controller	PCIO@0:9:0:0	pci1440,4904	PciRoot(0x0)/Pci(0x0/0x09/0x0)
44:00.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:44:0:0	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x44/0x0)
44:01.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:44:0:1	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x44/0x1)
44:02.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:44:0:2	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x44/0x2)
44:04.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:44:0:4	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x44/0x4)
48:00.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:0	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x0)
48:01.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:1	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x1)
48:02.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:2	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x2)
48:04.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:4	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x4)
48:08.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:8	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x8)
48:0C.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:C	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0xC)
48:10.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:10	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x10)
48:14.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:14	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x14)
48:18.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:18	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x18)
48:1C.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:1C	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x1C)
48:20.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:20	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x20)
48:24.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:24	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x24)
48:28.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:28	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x28)
48:34.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:34	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x34)
48:38.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:38	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x38)
48:44.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:44	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x44)
48:48.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:48	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x48)
48:54.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:54	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x54)
48:60.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:60	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x60)
48:64.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:64	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x64)
48:68.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:68	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x68)
48:74.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:74	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x74)
48:78.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:78	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x78)
48:84.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:84	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x84)
48:88.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:88	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x88)
48:94.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:94	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x94)
48:98.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:48:0:98	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x48/0x98)
49:04.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:4	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x4)
49:08.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:8	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x8)
49:0C.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:C	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0xC)
49:10.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:10	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x10)
49:14.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:14	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x14)
49:18.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:18	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x18)
49:1C.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:1C	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x1C)
49:20.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:20	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x20)
49:24.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:24	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x24)
49:28.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:28	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x28)
49:34.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:34	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x34)
49:38.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:38	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x38)
49:44.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:44	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x44)
49:48.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:48	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x48)
49:54.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:54	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x54)
49:60.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:60	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x60)
49:64.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:64	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x64)
49:68.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:68	pci8086,15e4	PciRoot(0x0)/Pci(0x0/0x49/0x68)
49:74.0	0x8086	0x15E4	0x0000	0x0000	L1	Intel Corporation	Bridge	PCIO@0:49:0:74	pci8086,15e4	