

Erledigt

Suche TrueSuite 5.5.0.5 (Authentec)

Beitrag von „Griven“ vom 6. Mai 2013, 23:17

Hi IBM,

ich habe eigentlich (zumindest bewusst) nichts an USB geändert. Bei mir in der DSDT sieht es so aus:

Code

1. Device (USB0)
2. {
3. Name (_ADR, 0x001D0000)
4. Name (_S3D, 0x02)
5. Name (RID, Zero)
6. OperationRegion (U0CS, PCI_Config, 0xC4, 0x04)
7. Field (U0CS, DWordAcc, NoLock, Preserve)
8. {
9. U0EN, 2,
10. Offset (0x04)
11. }
12. Name (_PR0, Package (0x01))
13. {
14. ^LPC.EC.PUBS
15. })
16. Name (_PR1, Package (0x01))
17. {
18. ^LPC.EC.PUBS
19. })
20. Name (_PR2, Package (0x01))
21. {
22. ^LPC.EC.PUBS
23. })
24. Name (_PRW, Package (0x03))
25. {
26. 0x03,
27. 0x03,
28. ^LPC.EC.PUBS
29. })

```
30. Method (_DSM, 4, NotSerialized)
31. {
32. Store (Package (0x02)
33. {
34. "device-id",
35. Buffer (0x04)
36. {
37. 0x34, 0x3A, 0x00, 0x00
38. }
39. }, Local0)
40. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
41. Return (Local0)
42. }
43. }
44. Device (USB1)
45. {
46. Name (_ADR, 0x001D0001)
47. Name (_S3D, 0x02)
48. Name (RID, Zero)
49. OperationRegion (U1CS, PCI_Config, 0xC4, 0x04)
50. Field (U1CS, DWordAcc, NoLock, Preserve)
51. {
52. U1EN, 2,
53. Offset (0x04)
54. }
55. Name (_PRW, Package (0x02)
56. {
57. 0x04,
58. 0x03
59. })
60. Device (URTH)
61. {
62. Name (_ADR, Zero)
63. Device (UPEX)
64. {
65. Name (_ADR, 0x02)
66. }
67. }
68. Method (_DSM, 4, NotSerialized)
69. {
70. Store (Package (0x02)
71. {
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```
72. "device-id",
73. Buffer (0x04)
74. {
75. 0x35, 0x3A, 0x00, 0x00
76. }
77. }, Local0)
78. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
79. Return (Local0)
80. }
81. }
82. Device (USB2)
83. {
84. Name (_ADR, 0x001D0002)
85. Name (_S3D, 0x02)
86. Name (RID, Zero)
87. OperationRegion (U2CS, PCI_Config, 0xC4, 0x04)
88. Field (U2CS, DWordAcc, NoLock, Preserve)
89. {
90. U2EN, 2,
91. Offset (0x04)
92. }
93. Name (_PR0, Package (0x01)
94. {
95. ^LPC.EC.PUBS
96. })
97. Name (_PR1, Package (0x01)
98. {
99. ^LPC.EC.PUBS
100. })
101. Name (_PR2, Package (0x01)
102. {
103. ^LPC.EC.PUBS
104. })
105. Name (_PRW, Package (0x03)
106. {
107. 0x0C,
108. 0x03,
109. ^LPC.EC.PUBS
110. })
111. Method (_DSM, 4, NotSerialized)
112. {
113. Store (Package (0x02)
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114. {
115. "device-id",
116. Buffer (0x04)
117. {
118. 0x36, 0x3A, 0x00, 0x00
119. }
120. }, Local0)
121. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
122. Return (Local0)
123. }
124. }
125. Device (USB3)
126. {
127. Name (_ADR, 0x001A0000)
128. Name (_S3D, 0x02)
129. Name (RID, Zero)
130. OperationRegion (U3CS, PCI_Config, 0xC4, 0x04)
131. Field (U3CS, DWordAcc, NoLock, Preserve)
132. {
133. U3EN, 2,
134. Offset (0x04)
135. }
136. Name (_PRW, Package (0x02)
137. {
138. 0x0E,
139. 0x03
140. })
141. Method (_DSM, 4, NotSerialized)
142. {
143. Store (Package (0x02)
144. {
145. "device-id",
146. Buffer (0x04)
147. {
148. 0x37, 0x3A, 0x00, 0x00
149. }
150. }, Local0)
151. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
152. Return (Local0)
153. }
154. }
155. Device (USB4)
```

156. {
157. Name (_ADR, 0x001A0001)
158. Name (_S3D, 0x02)
159. Name (RID, Zero)
160. OperationRegion (U4CS, PCI_Config, 0xC4, 0x04)
161. Field (U4CS, DWordAcc, NoLock, Preserve)
162. {
163. U4EN, 2,
164. Offset (0x04)
165. }
166. Name (_PR0, Package (0x01)
167. {
168. ^LPC.EC.PUBS
169. })
170. Name (_PR1, Package (0x01)
171. {
172. ^LPC.EC.PUBS
173. })
174. Name (_PR2, Package (0x01)
175. {
176. ^LPC.EC.PUBS
177. })
178. Name (_PRW, Package (0x03)
179. {
180. 0x05,
181. 0x03,
182. ^LPC.EC.PUBS
183. })
184. Device (URTH)
185. {
186. Name (_ADR, Zero)
187. Device (UPDK)
188. {
189. Name (_ADR, 0x02)
190. }
191. }
192. Method (_DSM, 4, NotSerialized)
193. {
194. Store (Package (0x02)
195. {
196. "device-id",
197. Buffer (0x04)

198. {
199. 0x38, 0x3A, 0x00, 0x00
200. }
201. }, Local0)
202. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
203. Return (Local0)
204. }
205. }
206. Device (EHC0)
207. {
208. Name (_ADR, 0x001D0007)
209. Name (_S3D, 0x03)
210. Name (RID, Zero)
211. OperationRegion (U7CS, PCI_Config, 0x60, 0x04)
212. Field (U7CS, DWordAcc, NoLock, Preserve)
213. {
214. Offset (0x02),
215. PWKI, 1,
216. PWUC, 6,
217. Offset (0x04)
218. }
219. Name (_PR0, Package (0x01)
220. {
221. ^LPC.EC.PUBS
222. })
223. Name (_PR1, Package (0x01)
224. {
225. ^LPC.EC.PUBS
226. })
227. Name (_PR2, Package (0x01)
228. {
229. ^LPC.EC.PUBS
230. })
231. Method (_INI, 0, NotSerialized)
232. {
233. Store (One, PWKI)
234. Store (0x0F, PWUC)
235. }
236. Method (_DSM, 4, NotSerialized)
237. {
238. Store (Package (0x08)
239. {

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240. "AAPL,clock-id",
241. Buffer (One)
242. {
243. 0x01
244. },
245. "device_type",
246. Buffer (0x05)
247. {
248. "EHCI"
249. },
250. "AAPL,current-available",
251. 0x04B0,
252. "AAPL,current-extra",
253. 0x02BC
254. }, Local0)
255. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
256. Return (Local0)
257. }
258. Name (_PRW, Package (0x03)
259. {
260. 0x0D,
261. 0x03,
262. ^LPC.EC.PUBS
263. })
264. Device (URTH)
265. {
266. Name (_ADR, Zero)
267. Device (UPEX)
268. {
269. Name (_ADR, 0x04)
270. }
271. }
272. }
273. Device (EHC1)
274. {
275. Name (_ADR, 0x001A0007)
276. Name (_S3D, 0x03)
277. Name (RID, Zero)
278. OperationRegion (U8CS, PCI_Config, 0x60, 0x04)
279. Field (U8CS, DWordAcc, NoLock, Preserve)
280. {
281. Offset (0x02),
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```
282. PWKI, 1,
283. PWUC, 4,
284. Offset (0x04)
285. }
286. Name (_PR0, Package (0x01))
287. {
288. ^LPC.EC.PUBS
289. })
290. Name (_PR1, Package (0x01))
291. {
292. ^LPC.EC.PUBS
293. })
294. Name (_PR2, Package (0x01))
295. {
296. ^LPC.EC.PUBS
297. })
298. Method (_INI, 0, NotSerialized)
299. {
300. Store (One, PWKI)
301. Store (0x0F, PWUC)
302. }
303. Method (_DSM, 4, NotSerialized)
304. {
305. Store (Package (0x08))
306. {
307. "AAPL,clock-id",
308. Buffer (One)
309. {
310. 0x02
311. },
312. "device_type",
313. Buffer (0x05)
314. {
315. "EHCI"
316. },
317. "AAPL,current-available",
318. 0x04B0,
319. "AAPL,current-extra",
320. 0x02BC
321. }, Local0)
322. DTGP (Arg0, Arg1, Arg2, Arg3, RefOf (Local0))
323. Return (Local0)
```



```
324. }
325. Name (_PRW, Package (0x03)
326. {
327. 0x0D,
328. 0x03,
329. ^LPC.EC.PUBS
330. })
331. Device (URTH)
332. {
333. Name (_ADR, Zero)
334. Device (UPDK)
335. {
336. Name (_ADR, 0x04)
337. }
338. }
339. }
340. }
```

Alles anzeigen

Der FPR wir wie folgt von OSX erkannt:

Code

1. USB-Bus:
- 2.
- 3.
4. Ort für Host-Controller: USB (Integriert)
5. Treiber für Host-Controller: AppleUSBHCI
6. PCI Geräte-ID: 0x3a37
7. PCI Revisions-ID: 0x0003
8. PCI Hersteller-ID: 0x8086
9. Bus-Nummer: 0x1a
- 10.
- 11.
12. Biometric Coprocessor:
- 13.
- 14.
15. Produkt-ID: 0x2016
16. Hersteller-ID: 0x0483 (STMicroelectronics)
17. Version: 0.01
18. Geschwindigkeit: Bis zu 12 MBit/s
19. Hersteller: STMicroelectronics
20. Standort-ID: 0x1a200000 / 2

21. Verfügbare Stromstärke (mA): 500
22. Erforderliche Stromstärke (mA): 100

Alles anzeigen

Wobei ich natürlich nicht weiß ob das TP Bios hier vielleicht anders gestrickt ist als das Deines, ich nehme an LG?!