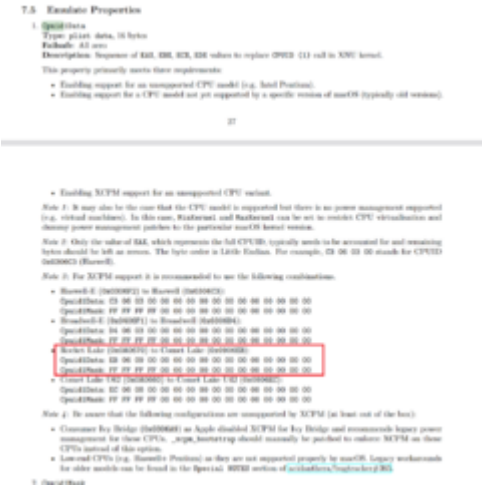
Rocket-Lake-S CPU in Z490-Board - wie es geht...

Beitrag von „Bob-Schmu“ vom 2. Juli 2021, 01:00

Warum soll er was dagegen haben, ein Blick in die Configuration.pdf von OpenCore und du hättest dieses auch selbst herausgefunden.



7.5 Emulate Properties

1. Emulate
   Type: plist data, 16 bytes
   Failsafe: All zero
   Description: Sequence of EAX, EBX, ECX, EDX values to replace CPUID (1) call in XNU kernel.

   This property primarily meets three requirements:

   - Enabling support for an unsupported CPU model (e.g. Intel Pentium).
   - Enabling support for a CPU model not yet supported by a specific version of macOS (typically old versions).

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- Enabling XCPM support for an unsupported CPU variant.

Note 1: It may also be the case that the CPU model is supported but there is no power management supported (e.g. virtual machines). In this case, MinKernel and MaxKernel can be set to restrict CPU virtualisation and dummy power management patches to the particular macOS kernel version.

Note 2: Only the value of EAX, which represents the 3rd CPUID, typically needs to be accounted for and remaining bytes should be left as zeroes. The byte order is Little Endian. For example, C3 06 03 00 stands for CPUID 0x30006C3 (Haswell).

Note 3: For XCPM support it is recommended to use the following combinations.

- Haswell-E (0x30006F2) to Haswell (0x40306C3):
  OpenKernelData: C3 06 03 00 00 00 00 00 00 00 00 00 00 00 00 00
  OpenKernelMask: FF FF FF FF 00 00 00 00 00 00 00 00 00 00 00 00
- Broadwell-E (0x40606F1) to Broadwell (0x40606D4):
  OpenKernelData: D4 06 06 00 00 00 00 00 00 00 00 00 00 00 00 00
  OpenKernelMask: FF FF FF FF 00 00 00 00 00 00 00 00 00 00 00 00
- Rocket Lake (0x90806C0) to Comet Lake (0x90000A0):
  OpenKernelData: E0 06 09 00 00 00 00 00 00 00 00 00 00 00 00 00
  OpenKernelMask: FF FF FF FF 00 00 00 00 00 00 00 00 00 00 00 00
- Comet Lake V82 (0x90000A0) to Comet Lake V82 (0x90000A5):
  OpenKernelData: EC 06 09 00 00 00 00 00 00 00 00 00 00 00 00 00
  OpenKernelMask: FF FF FF FF 00 00 00 00 00 00 00 00 00 00 00 00

Note 4: Be aware that the following configurations are unsupported by XCPM (at least out of the box):

- Consumer Ivy Bridge (0x30006A0) as Apple disabled XCPM for Ivy Bridge and recommends legacy power management for these CPUs. _xcpm_bootstrap should manually be patched to enforce XCPM on these CPUs instead of this option.
- Low-end CPUs (e.g. Haswell+ Pentium) as they are not supported properly by macOS. Legacy workarounds for older models can be found in the Special: MSR section of acidanthera/bugtracker#365.

7. Quirks Block