

Sleep-Problem unter Monterey mit Z490-Chipsatz

Beitrag von „anonymous_writer“ vom 20. Februar 2022, 09:06

Das ganze auch mal hiermit versucht?

SSDT-GPRW:

Code

```
1. // For solving instant wake by hooking GPRW or UPRW
2.
3. #ifndef NO_DEFINITIONBLOCK
4. DefinitionBlock("", "SSDT", 2, "hack", "_GPRW", 0)
5. {
6. #endif
7. External(XPRW, MethodObj)
8. External(RMCF.DWOU, IntObj)
9.
10. // In DSDT, native GPRW is renamed to XPRW with Clover binpatch.
11. // As a result, calls to GPRW land here.
12. // The purpose of this implementation is to avoid "instant wake"
13. // by returning 0 in the second position (sleep state supported)
14. // of the return package.
15. Method(GPRW, 2)
16. {
17. For (,,)
18. {
19. // when RMCF.DWOU is provided and is zero, patch disabled
20. If (CondRefOf(\RMCF.DWOU)) { If (!\RMCF.DWOU) { Break }}
21. // either RMCF.DWOU not provided, or is non-zero, patch is enabled
22. If (0x6d == Arg0) { Return (Package() { 0x6d, 0, }) }
23. If (0x0d == Arg0) { Return (Package() { 0x0d, 0, }) }
24. Break
25. }
26. Return (XPRW(Arg0, Arg1))
27. }
28. #ifndef NO_DEFINITIONBLOCK
29. }
30. #endif
```

31. //EOF

Alles anzeigen

Patch dazu / Muss zur eigenen DSDT passen wo GPRW vorkommt:

