

# USB mittels SSDT deklarieren

Beitrag von „EdD1024“ vom 16. April 2022, 15:02

Besten Dank

| Type | ID   | Name  | Series  | Header... | Device... |
|------|------|---|---------|-----------|-----------|
| XHC  | 0x00 | JH6540 Thunderbolt 3 USB Controller (C-Host) (Alpine Ridge AC 2016) | Unknown | 0x8006    | 0x7004    |
| XHC  | 0x14 | Cannon Lake PCH USB 3.1 xHCI Host Controller                        | 300     | 0x8006    | 0xA360    |

  

| Type | Name | Location ID | Port | Connector | Gen Speed | Device   | Comment                      |
|------|------|-------------|------|-----------|-----------|----------|------------------------------|
| XHC  | H501 | 0x14700000  | 0x01 | USB3      | 3         | Unknown  | Top Right USB-A (3.0)        |
| XHC  | H502 | 0x14700000  | 0x02 | UsbC-0a   | 3         | 480 Mbps | Middle Right USB-C (3.1)     |
| XHC  | H503 | 0x14700000  | 0x03 | UsbC-0a   | 3         | Unknown  | Bottom Right USB-C (3.1)     |
| XHC  | S503 | 0x00000000  | 0x03 | UsbC-0a   | 3         | 10 Gbps  | USB 4 Hub                    |
| XHC  | H504 | 0x14800000  | 0x04 | Internal  | 3         | 480 Mbps | Integrated Camera            |
| XHC  | S504 | 0x00000000  | 0x04 | UsbC-0a   | 3         | Unknown  | Integrated Camera (Internal) |
| XHC  | H505 | 0x14800000  | 0x05 | Internal  | 3         | 480 Mbps | Goodix FingerPrint Dev...    |
| XHC  | H506 | 0x14800000  | 0x05 | Internal  | 3         | 12 Mbps  | ICLUSBHostDevice             |
| XHC  | S507 | 0x14700000  | 0x07 | USB3      | 3         | Unknown  | Top Right USB-A (3.0)        |

Wenn ich das richtig sehe hängen SS03+4 am Thunderbolt und die anderen am anderen USB 3.1-Controller. Dummerweise heißen beide XHC. Das ganze macht immer wieder Probleme mit Sleep. Ich muss das Thema definitiv demnächst mal angehen.