

Umsetzung prüfen **DMAR patching**

Beitrag von „xayide“ vom 29. September 2023, 15:08

I drop DMAR with OpenCore and patched my DMAR, but when I enable DMAR patch the computer Kernel panics at boot.

I remove DMAR with this:

Code

1. <dict>
2. <key>All</key>
3. <false/>
4. <key>Comment</key>
5. <string>Delete standard DMAR table</string>
6. <key>Enabled</key>
7. <true/>
8. <key>OemTableId</key>
9. <data>
10. </data>
11. <key>TableLength</key>
12. <integer>0</integer>
13. <key>TableSignature</key>
14. <data>
15. RE1BUg==
16. </data>

Alles anzeigen

I remove below and save it as SSDT-DMAR.aml and add it to SSDT-patches in OpenCore. But it goes KP. Should I remove more or less, anything else in there? I attach the file to this post.

Code

1. [198h 0408 2] Subtable Type : 0001 [Reserved Memory Region]
2. [19Ah 0410 2] Length : 0020
- 3.
4. [19Ch 0412 2] Reserved : 0000
5. [19Eh 0414 2] PCI Segment Number : 0000
6. [1A0h 0416 8] Base Address : 000000006B943000

7. [1A8h 0424 8] End Address (limit) : 000000006B954FFF
- 8.
9. [1B0h 0432 1] Device Scope Type : 01 [PCI Endpoint Device]
10. [1B1h 0433 1] Entry Length : 08
11. [1B2h 0434 2] Reserved : 0000
12. [1B4h 0436 1] Enumeration ID : 00
13. [1B5h 0437 1] PCI Bus Number : 00
- 14.
15. [1B6h 0438 2] PCI Path : 14,00

Alles anzeigen