

# macOS 15 Sequoia Beta im Test

Beitrag von „Arkturus“ vom 12. Juli 2024, 16:55

Glückwunsch. Ich hatte gerade mal einen regnerischen Vormittag genutzt und wollte bei meinem HW-Desktop die iGPU wieder zum laufen bringen. Aber es klappt nicht mehr. Immer wenn ich über iGPU booten will, läuft das in AHCI error die Zeilen wiederholen sich immer wieder. Ich hab's jetzt endgültig abgeschlossen.

```
DMCI11PRT11001F02301 #boot-commands: (5301) Port 3 - called, deviceWithError 0, errCode = 0x0030000
DMCI11PRT11001F02301 #boot-commands: (5301) Port 3 - called, deviceWithError 0, errCode = 0x0030000
Setting BTCoex Profile: band0
DMCI11PRT11001F02301 #boot-commands: (5301) Port 3 - called, deviceWithError 0, errCode = 0x0030000
ProfileID: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
ProfileID2: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
Setting BTCoex Profile: band2
ProfileID: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
Setting BTCoex Config: enable_2G:1; profile_2G:0; enable_5G:1; profile_5G:0
DMCI11PRT11001F02301 #boot-commands: (5301) Port 3 - called, deviceWithError 0, errCode = 0x0030000
DMCI11PRT11001F02301 #boot-commands: (5301) Port 3 - called, deviceWithError 0, errCode = 0x0030000
DMCI11PRT11001F02301 #boot-commands: (5301) Port 3 - called, deviceWithError 0, errCode = 0x0030000
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Setting BTCoex Profile: band0
nonPnpClient: Invalid userClient type request: 0ProfileID: mode:0; dssense:0; dssense_level:0; chain_
ProfileID2: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
Setting BTCoex Profile: band2
ProfileID: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
Setting BTCoex Config: enable_2G:1; profile_2G:0; enable_5G:1; profile_5G:0
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Setting BTCoex Profile: band0
ProfileID: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
ProfileID2: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
Setting BTCoex Profile: band2
ProfileID: mode:0; dssense:0; dssense_level:0; chain_power_offset:0,0,0,0
Setting BTCoex Config: enable_2G:1; profile_2G:0; enable_5G:1; profile_5G:0
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```