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patches reduced

# Patch string format
# FileGuid SectionType PatchType:FindPatternOrOffset:ReplacePattern
# Please ensure that the latest symbol in patch string is space

# Possible section types:
# PE32 image 10
# Position-independent code 11
# TE Image 12
# DXE Dependency 13
# Version information 14
# User interface string 15
# 16-bit code 16
# Guided freeform 18
# Raw data 19
# PEI Dependency 1B
# SMM Dependency 1C
# Please do not try another section types, it can make the resulting image broken

# Possible patch types:
# P - pattern-based, first parameter is a pattern to find, second - a pattern to replace
# O - offset-based, first parameter is hexadecimal offset, second - a pattern to replace
# Patterns can have . as "any possible value" symbol

#-----
# OSX CPU Power Management patches
# Remove lock from MSR 0xE2 register
#-----

# PpmInitialize | Broadwell-E
3FFCAE95-23CF-4967-94F5-16352F68E43B 10 P:0FBA6C24400F:0FBA7424400F

# PpmInitialize | Skylake-X
3FFCAE95-23CF-4967-94F5-16352F68E43B 10 P:742CB9E2000000:752CB9E2000000

# CpuInitPei | Skylake-X
01359D99-9446-456D-ADA4-50A711C03ADA 12 P:BE0080000023CE0B:BE0000000023CE0B

# PpmInitialize | 0xE2 & 0x1AA unlock for C422 Chipsets & Some X299 Chipsets
3FFCAE95-23CF-4967-94F5-16352F68E43B 10 P:4889442430...0FBAE8:4889442430...0FBAE0
3FFCAE95-23CF-4967-94F5-16352F68E43B 10 P:4889442450...0FBAE8:4889442450...0FBAE0

# PpmInitialize | 0xE2 & 0x1AA unlock for C620 Chipsets
3FFCAE95-23CF-4967-94F5-16352F68E43B 10 P:480BC2BA00200000:480BC2BA00000000
3FFCAE95-23CF-4967-94F5-16352F68E43B 10 P:480BC2BA00800000:480BC2BA00000000
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Ich hab aus der Liste mal den Kram rausgestrichen, den ich sicher nicht brauche. Wenn ich mich recht entsinne, ist der Chipsatz C620 der für das Sage-Board, nicht aber für die "normalen" X299er.